

# Andrew Karpenko

(206) 659-6699  
akarpy@ee.washington.edu

**Online Portfolio**  
<http://www.filikagroup.com/>

<b>Education</b>	University of Washington, Seattle, WA <i>B.S. in Electrical Engineering, Embedded Systems, Controls Mathematics Minor</i>	2007-2011
	Washington State University, Pullman, WA <i>Running Start, Russian, Economics</i>	2006-2007
<b>Employment</b>	<b>Web Publisher</b> , Creative + Communications, Seattle, WA <ul style="list-style-type: none"><li>• Build web applications and CMS</li><li>• Primary contact for clients</li><li>• Extensive work in interdisciplinary teams</li><li>• Accessibility testing and Section 508 compliance</li></ul>	2007-Present
	<b>Owner</b> , Filika Group, Pullman, WA <ul style="list-style-type: none"><li>• Design and build websites, web applications</li><li>• Handle all customer requests</li><li>• Manage all aspects of a business</li></ul>	2005-Present
	<b>Supervisor</b> , Happy Day Co., Lewiston, ID <ul style="list-style-type: none"><li>• Train all new employees</li><li>• In-charge of store in managers absence</li></ul>	2006-2007
<b>Skills</b>	<b>Spoken Languages</b> <ul style="list-style-type: none"><li>• Fluent in Ukrainian, Russian and English</li><li>• Learning Spanish</li></ul>	
	<b>Technical Languages</b> <ul style="list-style-type: none"><li>• C, ObjectiveC, Assembly (MIPS &amp; AVR), Java</li><li>• Verilog</li><li>• Extensive knowledge of the LAMP Stack</li><li>• HTML, DHTML, CSS, JavaScript+jQuery</li></ul>	
	<b>Hardware</b> <ul style="list-style-type: none"><li>• Embedded systems development on Atmel AVR, PIC, MIPS processors</li><li>• FPGA development using Verilog and Synplify Pro</li><li>• Analog and Digital ICs, Digital Logic, and FSMs design experience</li><li>• Extensive soldering experience, including surface-mount components</li><li>• Extensive debugging experience using oscilloscopes</li></ul>	
	<b>Software</b> <ul style="list-style-type: none"><li>• Microsoft Office and Adobe Creative Suites</li><li>• LaTeX</li><li>• SolidWorks, AutoCad, and Maya</li><li>• Final Cut Pro</li><li>• SPICE, Altium Designer, Matlab and LabVIEW</li></ul>	